

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. The listing was supplied to us by IBM Corporate Patent Operations.

France

258,514	J. Calvignac and P. Secondo	Optimized Bandwith Allocation Mechanism between Circuit Slots and Packet Bit Stream in a Communication Network
263,220	B. Auda	Method of Forming a Via-hole Having a Desired Slope in a Photoresist Masked Composite Insulating Layer
325,077	D. Wind and Y. Granger	Scanner Interface for the Line Adapters of a Communication Controller
325,078	J. M. Munier, M. Peyronnenc, and M. Poret	Mechanism for Error Detection and Reporting on a Synchronous Bus

Germany

9,202,678.8	W. Loef	Clean Room Box
9,202,913.2	W. Bruegmann and W. Ruppert	Test Document for Scanners or Similar Articles
3,781,157,608	H. E. Korth	Beam-analyzer for Optical Storage Devices

Japan

855,596	K. Yamazaki and Y. Manabe	14' Mono Display Terminal
1,684,015	B. Nakagawa and K. Nojima	Display Apparatus
1,689,246	A. Kuroda, K. Sugawara, and M. Nishimura	Speech Recognition Method
1,689,273	M. Nishimura	Speaker Adaptation for HMM Based Speech Recognition
1,693,468	K. Ohmori	Image Processing System
1,693,470	J. Yamaguchi and T. Akai	Image Processing Apparatus
1,697,604	A. Miyazawa	Scan Conversion Method for Painting a Circle or an Ellipse

United Kingdom

WO92/10893	G. J. Cockburn, I. D. Judd, R. J. Grainger, and V. V. Shah	Method of Error Recovery in a Data Communication System
WO92/10894	I. D. Judd, R. Beer, and V. V. Shah	Optimised Method of Data Communication and System Employing Same
WO92/15053	N. Apperley and P. A. Buckland	Data Transfer within a Data Storage Subsystem
298,166	M. J. Healey	Flat File Layout
2,021,132	M. D. Oates	Visual Display Unit
2,021,684	M. H. Sharp	Computer System Unit
2,021,685	M. H. Sharp	Computer System Unit

United States

- 5,110,409 F. D. Egitto and W. E. Mlynko
5,111,713 T. J. Cameron, E. G. Erickson, and C. H. Hamphill
5,111,723 F. Andrusch, S. Beerhalter, W. F. Mueller, and F. B. Zykoff
5,112,119 J. R. Dewitt and K. K. Cooke
5,112,699 C.-A. Chang
5,112,765 C. Cederbaum, R. Chanclou, M. Combes, P. Mone, and V. Vallet
5,113,153 M. Soyuer
5,113,180 S. Gupta, S. P. Larky, A. W. Peevers, and J. C. St. Clair
5,113,341 M. J. Kozol, C. Lim, W. L. Strong, Jr., and W. J. Tracey
5,113,342 A. Zamora
5,113,401 P. R. Chevillat, E. Eleftheriou, and G. Maiwald
5,113,403 T. Lu and T. R. Block
5,113,467 G. W. Peterson and B. W. Weidle
5,113,482 K. J. Lynne
5,113,489 P. K. Cihwisky and C. S. Jaw
5,113,508 R. D. Groves and D. P. Tuttle
5,113,509 H. M. Pennings and H. F. Wijnen
5,113,519 D. W. Johnson, S. P. Morgan, and T. A. Smith
5,113,522 J. M. Dinwiddie, Jr., B. J. Freeman, L. E. Grice, J. M. Loffredo, K. R. Sanderson, and G. A. Suarez
5,114,529 A. Masuyama and T. Saitoh
5,114,757 H. Linde and R. A. Previti-Kelly
5,115,090 H. S. Sachdev, J. P. Hummel, K. G. Sachdev, L. L. Linehan, R. Kwong, and R. N. Lang
5,115,228 R. H. Harris, R. G. Minor, and R. W. Price
5,115,664 A. P. Praino, M. Sri-Jayantha, S. Root, and S. G. Hegde
5,115,964 J. Funari, J. Goldfuss, and J. G. Ameen
5,116,459 L. H. Wirtz, M. Kordus, and M. S. Schneider
5,116,462 G. Kraus, H. Weiss, J. Greschner, J. W. Bartha, O. Wolter, and T. Bayer
5,116,583 J. S. Batchelder, K. D. Murray, P. C. Hobbs, R. J. Miller, and V. P. Gross
5,117,071 E. C. Greanias, F. L. Stein, R. Donaldson, and M. Gray
5,117,129 D. L. Jallice, J. A. Hoffman, R. G. Richards, and Y. K. Puri
5,117,199 C. K. Wang, D. Freitas, and R. Khan
5,117,271 B. S. Meyerson, D. D. Tang, J. H. Comfort, P. F. Lu, T. C. Chen, and Y. C. Sun
5,117,275 A. Lanzetta, H. M. Tong, I. C. Noyan, M. F. Bregman, M. J. Palmer, and R. Horton
5,117,320 A. Tobari, K. Kurachi, and K. Ogasawara
5,117,384 R. A. Drehmel and S. A. Hilker
5,117,385 R. L. Gee
5,117,430 N. C. Berglund
5,117,457 L. D. Comerford, L. I. Levy, P. G. Ledermann, and S. R. White
5,117,486 A. R. Clark, J. Valashinas, J. E. Hughes, and J. P. Higham
Enhanced Plasma Etching
Dynamically Balanced Rotary Unit
Punch Apparatus with Positive Slug Removal
Support Structure for Devices in a Computer Apparatus
Metal-Metal Epitaxy on Substrates and Method of Making
Method of Forming Stacked Tungsten Gate PFET Devices and Structures Resulting Therefrom
High-Frequency Monolithic Oscillator Structure for Third-Overtone Crystals
Virtual Display Adapter
Technique for Creating and Expanding Element Marks in a Structured Document
Computer Method for Executing Transformation Rules
Block Coding Scheme for Fractional-Bit Transmission
Bidirectional Free-Space Optical Bus for Electronics Systems
Laser Transmitter Interlock
Neural Network Model for Reaching a Goal State
On-line Performance Monitoring and Fault Diagnosis Technique for Direct Current Motors as Used in Printer Mechanisms
Data Cache Initialization
Method of Changing Data on Disk
Intenance of File Attributes in a Distributed Data Processing System
Data Processing System with Resource Management for Itself and for an Associated Alien Processor
Plasma Processing Method and Apparatus
Enhancement of Polyimide Adhesion on Reactive Metals
Low TCE Polyimides
Shuttered Display Panel
Tunable Feedback Transducer for Transient Friction Measurement
Method for Bonding Thin Film Electronic Device
Novel Processes for Electrically Conductive Decals Filled with Organic Insulator Material
Method of Producing Micromechanical Sensors for the AFM/STM Profilometry
Suppression of Particle Generation in a Modified Clean Room Corona Air Ionizer
Stylus Sensing System
A CMOS Off Chip Driver for Fault Tolerant Cold Sparing
Fully Differential Follower Using Operation Amplifier
New Low Capacitance Bipolar Junction Transistor and Fabrication Process Therefor
Electronic Substrate Multiple Location Conductor Attachment Technology
Magnetorestrictive Head with D. C. Bias Magnetic Field
Method and Apparatus for Exponent Adder
Table Lookup Multiplier with Digital Filter
Apparatus and Method for Communicating between Nodes in a Network
Tamper Resistant Packaging for Information Protection in Electronic Circuitry
Buffer for Packetizing Blocks of Data with Different Sizes Rates Received from First Processor Before Transferring to Second Processor

- 5,117,494 A. Gach, J. Dugre, J. P. Dugre, and M. Costes
- 5,117,555 R. E. Hunt and V. E. Whitehead
- 5,117,726 B. S. McDonald and R. J. Lisle
- 5,118,027 C. J. Braun, G. C. Correia, S. J. Konecke-Lease, and R. W. Cummings
- 5,118,299 F. C. Burns, J. J. Kaufman, D. E. King, and A. D. Knight
- 5,118,364 J. Frazier
- 5,118,382 J. Cronin and M. N. Lakritz
- 5,118,972 L. Wissel and T. Zittritsch
- 5,119,059 K. R. Covi, W. J. Petrowsky, and J. H. Spreen
- 5,119,082 A. W. Peevers, L. Lumelsky, and S. Choi
- 5,119,113 R. Prakash and L. Wolfe
- 5,119,157 D. L. Harame, B. S. Meyerson, and J. M. C. Stork
- 5,119,186 A. Edgar and J. J. Deacon
- 5,119,250 M. R. Green, J. Stephenson, and M. C. Stich
- 5,119,264 E. A. Lindfors
- 5,119,270 I. W. Bolton, A. N. Hamper, D. S. Gaunt, and D. J. Gray
- 5,119,291 W. J. Flannagan, R. M. Kern, J. E. Kulakowski, and R. E. Wagner
- 5,119,377 P. R. Cobb, C. J. Lennon, and K. J. Long
- 5,119,433 T. A. Will
- 5,119,470 F. D. Highland, C. T. Iwaskiw, J. D. Tani, and H. Gallivan
- 5,119,474 B. J. Beitel, M. S. Bishop, J. J. Deacon, R. D. Gordon, K. B. Smith, L. S. Walling, N. A. Burns, P. C. Yanker, M. D. Wilkes, and C. L. Haug
- 5,119,477 T. Ebbers
- 5,119,478 J. Calvignac, J. Feraud, B. Naudin, C. Pin, and E. Saint-Georges
- 5,119,480 D. R. Chisholm, S. E. Garcia, Jr., D. A. Kalman, R. S. Padgett, and R. D. Yoder
- 5,119,493 F. L. Janis, M. Williams, and D. S. Wang
- 5,119,498 A. B. McNeill, T. H. Newsom, N. A. Osborn, and E. M. Reid
- 5,119,711 D. J. Moore, J. L. Bell, R. J. Lisle, and S. C. Penn
- 5,119,759 C. Hicks
- 5,120,339 D. P. Seraphim, R. P. Schaffer, S. P. Mukherjee, and V. Markovich
- 5,120,418 M. J. Palmer, R. D. Topa, S. K. Kang, and T. C. Reiley
- 5,120,668 J. F. Shepard, L. Hsu, P. J. Tsang, and S. Ogura
- 5,120,987 R. C. Wong
- 5,121,001 A. H. Dansky and A. L. Mullgrav
- 5,121,181 J. M. Woodall and T. P. Smith
- 5,121,190 D. P. Seraphim, J. M. McCreary, R. Hsiao, and V. Markovich
- 5,121,235 H. Matino and T. Ueki
- 5,121,237 A. Ikeda, T. Koseki, and T. Ueki
- 5,121,263 G. J. Kerwin and J. W. Rae
- 5,121,292 D. F. Bell, E. J. Ossolinski, H. A. Carlson, J. R. Warnot, R. D. Birchall, V. C. Vasile, and W. S. Harris
- System for Selectively Detecting and Bypassing Inoperative Module Within a Daisy Chained Processing System
- Modular System and Method for Populating Circuit Boards
- Completely Variable "Q" Filter
- Method of Aligning and Mounting Solder Balls to a Substrate
- Cone Electrical Contact
- Solder Flux/Paste Compositions
- Elimination of Etch Stop Undercut
- BICMOS Gate Pull-Down Circuit
- Combined Differential and Common Mode Choke for a Power Supply
- Color Television Window Expansion and Overscan Correction for High-Resolution Raster Graphics Displays
- Spot-Defined Exposure System for a Laser Printer
- Semiconductor Device with Self-Aligned Contact to Buried Subcollector
- Color Mapping System and Method
- Method and Apparatus for Performing a Seek
- Keyboard Anti-Lockout Circuit
- Data Storage System with Device Dependent Flow of Cooling Air
- Modular Data Storage Directories for Large-Capacity Data Storage Units Wherein the Index to the Records in a Sector is Located in the Next Adjacent Sector
- System and Method for Software Error Early Detection and Data Capture
- Method and System for Locating the Amount Field on a Document
- Computer Based Inference Engine Device and Method Thereof for Integrating Backward Chaining and Forward Chaining Reasoning
- Computer-Based, Audio/Visual Creation and Presentation System and Method
- Memory Manager for Hierarchical Graphic Structures
- Parallel Processing Method and Device for Receiving and Transmitting HDLC SDLC Bit Streams
- Bus Master Interface Circuit with Transparent Preemption of a Data Transfer Controller
- System for Recording at Least One Selected Activity from a Selected Resource Object within a Distributed Data Processing System
- Feature Board with Automatic Adjustment to One of Two Bus Widths Based on Sensing Power Level at One Connection Contact
- Midi File Translation
- Method and Apparatus for Solder Nozzle Height Sensing
- Method for Fabricating a Low Thermal Expansion Coefficient Glass Fiber-Reinforced Polymer Matrix Composite Substrate & Composite Substrate
- Lead Frame Plating Apparatus for Thermocompression Bonding
- Inverse T-Gate FET Transistor
- Tunable Timer for Memory Arrays
- Low Power Push-Pull Driver
- Resonant Tunneling Photodetector for Long Wavelength Applications
- Solder Interconnection Structure on Organic Substrates
- Liquid Crystal Display Device Having Light Transmission Control Layer
- Liquid Crystal Display Device and Method of Manufacture
- Method and Apparatus for Determining the Error Rate of Magnetic Recording Disk Drives Having Amplitude Sampling Data Detection
- Quick Replacement Field Replaceable Unit (FRU) for Cryocooled Computer Logic

5,121,299	J. A. Frankeny, K. Hermann, and R. F. Frankeny	Method for Making High Density Wiring Circuit
5,121,319	J. Fath and K. M. Ferriter	Method and Apparatus for Selective Suspension and Resumption of Computer-Based Manufacturing Processes
5,121,354	B. D. Mandalia	Random Access Memory with Access on Bit Boundaries
5,121,357	D. Wendel and S. K. Wiedmann	Static Random Access Split-Emitter Memory Cell Selection Arrangement Using Bit Line Precharge
5,121,360	R. M. West and T. Williams	Video Random Access Memory Serial Port Access
5,121,369	T. Makansi	Method and Apparatus for Direct Overwriting Information on a Magneto-Optical Recording Medium Using Constant Laser Beam Modulated Magnetic Field
5,121,390	D. C. Kuhl, J. K. Farrell, J. S. Gordon, R. V. Jenness, T. E. Parker, and T. V. Lee	Integrated Data Link Controller with Synchronous Link Interface and Asynchronous Host Processor Interface
5,121,396	D. R. Irvin and K. K. Sy	Preservation of CRC Integrity Upon Intentional Data Alteration during Message Transmission
5,121,441	C. C. Tappert, J. Kim, T. Fujisaki, and T. E. Chefalas	Robust Prototype Establishment in an On-Line Handwriting Recognition System
5,121,475	B. J. Michalk, C. J. Rousse, G. L. Child, and R. J. Hrabik	Method of Displacing Messages Within a Computer
5,121,477	S. L. Watson and S. T. Koopmans	User Interface with System and Method for Specifying Same
5,121,483	C. J. Monahan, D. L. Willson, and M. L. Monahan	Virtual Drives in an Automated Storage Library
5,121,488	A. Ngai	Sequence Controller of an Instruction Processing Unit for Placing Said Unit in a Ready, Go, Hold, or Cancel State
5,121,489	P. Andrews	Tracing Method and Program Employing Such Method
5,121,494	D. M. Dias, J. Wolf, and P. S. Yu	Joining Two Database Relations on a Common Field in a Parallel Relational Database Field
5,121,497	R. H. Kerr and R. M. Mesnard	Automatic Generation of Executable Computer Code Which Commands Another Program to Perform a Task and Operator Modification
5,121,499	B. H. Machart, H. E. O'Steen, and R. A. McCaskill	Procedure for Controlling the Order of Editing Cells in a Spreadsheet
5,121,500	D. L. Arlington, H. H. Li, J. K. Morris, and J. K. Radcliffe	Preliminary Polling for Identification and Location of Removable/Replaceable Computer Components Prior to Power-Up
5,121,501	C. L. Raby, D. A. Bishop, J. D. Henson, J. R. Dyar, K. M. Herrington, M. H. Skelton, and R. S. Baumgartner	OS/2 Performance Measurement Technique
5,121,915	D. S. Miller, J. C. Yarrington, M. A. Duncan, and P. A. Hakenewerth	Document Processor Having Improved Throughput Capabilities
5,122,065	B. D. Washo, G. M. Jordhamo, and T. J. Dudek	Input Output Connector with Coaxial Shielding and Strain Relief
5,122,201	C. A. Okoro, J. D. Frazier, J. R. White, K. J. Pearsall, and R. Reich	Water-Soluble Solder Flux
5,122,439	E. Miersch and J. M. Park	Forming a Pattern on a Substrate
5,122,663	D. Rugar, H. J. Mamin, and T.-P. Chang	Compact, Integrated Electron Beam Imaging System
5,122,680	B. L. Stakely and R. L. Wenda	Precision Hysteresis Circuit
5,122,737	R. Clauberg	Method for Photoemission Inspection of Via Studs in Integrated Circuit Packages
5,122,744	R. H. Koch	Gradiometer Having a Magnetometer Which Cancels Background Magnetic Field from Other Magnetometers
5,122,784	F. C. Canova	Method and Apparatus for Color Conversion
5,123,029	C.-C. Huang, D. F. Bantz, and R. T. Cato	Broadcast-Initiated Bipartite Frame Multi-Access Protocol
5,123,104	C. Mohan and F. E. Levine	Method and Apparatus for Concurrent Modification of an Index Tree in a Transaction Processing System Utilizing Selective Indication of Structural Modification Operations
5,123,852	J. B. Gillett	Modular Electrical Connector
5,124,522	M. Kallmeyer, M. A. Booke, N. A. Rizk, and Y. W. Yau	Straight Wall via Hole Forming in Ceramic Green Sheets
5,124,561	J. G. Ryan, K. B. Kimmel, T. B. Faure, and T. D. Sullivan	Process for X-Ray Mask Warpage Reduction
5,124,571	J. D. Strom, J. F. Mikos, J. T. Trnka, and R. D. Gillingham	Data Processing System Having Four Phase Clocks Generated Separately on Each Processor Chip
5,124,573	R. C. Wong	Adjustable Clock Chopper/Expander Circuit
5,124,591	D. A. Kiesling, E. B. Eichelberger, F. Chan, and W. M. Chu	Low Power Push Pull Driver
5,124,693	C. Himmelstein and J. S. Wang	Three Dimensional Graphic Display with User Defined Vanishing Point

5,124,855	A. R. Hearn, G. N. Dew, M. G. Taylor, M. R. Hatchett, and M. W. Hall	Disk File with Isolation Mount
5,124,856	C. A. Brown, H. R. Wendt, and L. Volpe	Magnetic Recording Device with Unitary Filter Medium to Trap Organic and Inorganic Chemical Pollutants
5,124,867	H. Kitahori, T. Homatsuzaki, and Y. Masuda	Actuator Returning and Holding Device for a Disk Unit
5,124,927	J. C. Shaw, R. R. Jackson, T. Van Kessel, and W. Hopewell	Latent-Image Control of Lithography Tools
5,124,981	N. L. Golding	Access Control Method for DQDB Network
5,125,038	J. Ruppert and S. N. Meshkat	Face and Edge Trim Method for an Automatic Mesh Generation System
5,125,084	M. E. Dean, P. M. Bland, and R. M. Begun	Control of Pipelined Operation in a Microcomputer System Employing Dynamic Bus Sizing with 80386 Processor and 82385 Cache Controller
5,125,092	D. A. Prener	Method and Apparatus for Providing Multiple Condition Code Fields to Allow Pipelined Instructions Contention Free Access to Separate Condition Codes
5,125,096	R. D. Jackson, V. A. Norton, W. C. Brantley, Jr., and W. S. Groh	System for Implementing to a Packet Switch Protocol for a Multinode Data Communications Network Utilizing Separate Data and Control Paths
5,126,006	A. J. Watts, C. W. Kaanta, J. Ryan, J. E. Cronin, P. A. Farrar, R. A. Previti-Kelly, R. M. Geffken, R. R. Uttecht, and W. H. Guthrie	Plural Level Chip Masking
5,126,016	J. J. Glenning, K. G. Sakorafos, and W. P. Pawlowski	Circuitization of Polymeric Circuit Boards with Galvanic Removal of Chromium Adhesion Layers
5,126,192	D. W. Wang, L. N. Chellis, R. M. Japp, W. J. Rudik, and W. J. Summa	Flame Retardant, Low Dielectric Constant Microsphere Filled Laminate
5,126,690	A. S. Potemski and M. M. Bui	Phase Locked Loop Lock Detector Including Loss of Lock and Gain of Lock Detectors
5,127,008	D. W. Stout, T. H. Panner, M. E. Turner, P. S. Gillis, and R. W. Bassett	Integrated Circuit Driver Inhibit Control Test Method
5,127,011	A. J. Gruodis, C. A. Puntar, D. E. Hoffman, K. P. Szabo, and M. L. Combs	Per-Pin Integrated Circuit Test System Having N-Bit Interface
5,127,087	C. Kasiraj and T. Wolf	Method of Allowing the Transmission of Electronic Messages between Enrolled and Unenrolled Users of Computer Systems
5,127,091	C.-C. Lee, C. R. Moore, E. J. Boufarah, and G. F. Grohoski	System for Reducing Delay in Instruction Execution by Executing Branch Instructions in Separate Processor While Dispatching Subsequent Instructions to Primary Processors
5,127,571	B. L. Gutierrez and J. M. Sickler	Water Soluble Soldering Preflux and Method of Application
5,128,008	P. C. Chen and R. D. Weale	Method of Forming a Microelectronic Package Having a Copper Substrate
5,128,561	F. A. Montegari	Bipolar Receiver with ECL to CMOS Logic Level Conversion
5,128,568	G. D. Carpenter	Self-Biasing Timing Circuit for Achieving Long Time Delays
5,128,698	J. E. Cunningham, Jr. and J. L. Crawford	Boldness Control in an Electrophotographic Machine
5,128,798	D. F. Bowen, F. F. Tong, and P. A. Humblet	Addressable Wedge Etalon Filter
5,128,846	C. F. Reeves-Janzen, J. C. Scott, M. D. Ries, and R. N. Mills	Light Source
5,128,885	D. S. Wang, F. L. Janis, and M. Williams	Method for Automatic Generation of Document History Log Exception Reports in a Data Processing System
5,128,913	A. A. Fennema and S. D. Roberts	Acquiring Focus in an Optical System Using a Soft Focus Acquiring System
5,129,001	D. Nahamoo, J. R. Bellegarda, L. R. Bahl, M. A. Picheny, P. S. Gopalakrishnan, and P. V. De Souza	Method and Apparatus for Modeling Words with Multi-Arc Markov Models
5,129,043	P. C. Yue	Performance Improvement Tool for Rule Based Expert Systems
5,129,052	B. A. Barker, J. A. Stark, and T. R. Edel	Dynamic Selection of Recursively Nested Logical Element Data Formats
5,129,054	J. A. Hicke, J. K. Alstad, M. Wong, M. C. Lascelles, and S. P. Sherman	Specifying 3D Reference Points in 2D Graphic Displays
5,129,056	H. G. Kiel and S. T. Eagen	Method for Cursor Control of Scrolling Movements on Certain Computer Workstations
5,129,057	D. F. Murray and G. C. Strobe	Method for Presenting Compressed Electronic Day Calendar Information in an Interactive Information Handling System
5,129,064	A. M. de Nicolas, J. C. O'Quin III, and R. G. Fogg, Jr.	System and Method for Simulating the I/O of a Processing System
5,129,066	M. S. Schmookler	Bit Mask Generator Circuit Using Multiple Logic Units for Generating a Bit Mask Sequence
5,129,076	C. A. Peters, H. L. Freeman, R. G. Mustain, and T. M. Hoag	Optical Disk Library Control System Providing an Enhanced Initialization Technique