

NOTES:

1. FOR REFERENCE DRAWINGS REFER TO BILL(S) OF MATERIAL 01-W3370B01, B02.
2. UNLESS OTHERWISE SPECIFIED:
ALL RESISTORS ARE IN OHMS, $\pm 5\text{PCT}$,
1/4 WATT.
ALL CAPACITORS ARE IN UF.
ALL VOLTAGES ARE DC.
3. INTERRUPTED LINES CODED WITH THE SAME LETTER OR LETTER COMBINATIONS ARE ELECTRICALLY CONNECTED.
4. DEVICE TYPE NUMBER IS FOR REFERENCE ONLY. THE NUMBER VARIES WITH THE MANUFACTURER.
5. SPECIAL SYMBOL USAGE:
" DENOTES - ACTIVE LOW SIGNAL.
() DENOTES - ON BOARD SIGNAL.
6. INTERPRET DIAGRAM IN ACCORDANCE WITH AMERICAN NATIONAL STANDARDS INSTITUTE SPECIFICATIONS, CURRENT REVISION.
7. PART TYPES ARE ABBREVIATED IN THE FIELD OF THE DRAWING, FOR FULL PART TYPE, REFER TO TABLE 1.
8. CODE FOR SHEET TO SHEET REFERENCES IS AS FOLLOWS:
SHEET 8 A8 ZONE
9. OUTPUT PINS VARY AMONG VENDORS AND TO ACCOMMODATE THIS DIFFERENCE PINS 4 AND 8 HAVE BEEN CONNECTED.
10. DENOTES FRONT PANEL NOMENCLATURE.
11. COMPONENTS NOT INSTALLED: R16, R17, R18 AND R19.
12. U37 INSTALLED ON 01-W3370B03 AND 01-W3370B04 ONLY.

Y3	
U09	
S4	
R27	
P2	
J10	
DS4	
DL4	
C28	
HIGHEST NUMBER USED	NOT USED
REFERENCE DESIGNATIONS	

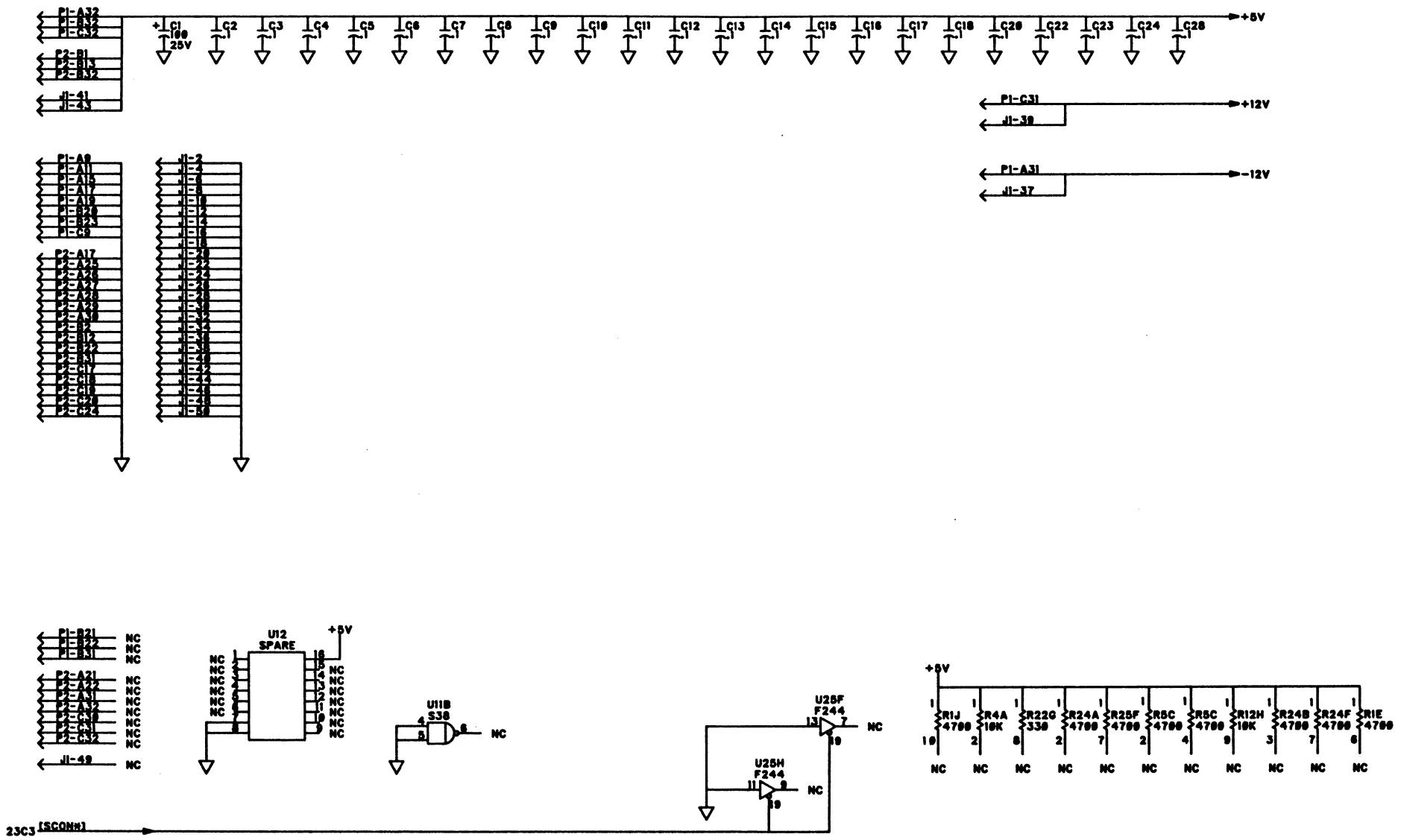
TABLE 1

REF DES	TYPE	GND	+5V	SH
DL1	100NS	7	14	18
DL2	40NS	7	14	18,25
DL3	30NS	7	14	8,13
DL4	25NS	7	14	21
U1	Z8038A	7	23	11
U2	74LS393	7	14	10
U3	74LS244	10	20	11
U4	74ALS845	10	20	15
U5	PAL18R6A	10	20	24
U6	74ALS845	10	20	15
U7	PAL18R6A	10	20	24
U8	PAL18R6R	10	20	11
U9	74F543	12	24	11
U10	PAL20L8A	12	24	10
U11	74S38	7	14	2,13,17,25
U12	SPARE			2
U13	74F74	7	14	8,13
U14	74F74	7	14	7,13
U15	74F80	7	14	13,17,18
U16	74F244	10	20	25
U17	74S240	10	20	14
U18	HM6284	14	20	10
U19	74F74	7	14	18
U20	74F164	7	14	8
U21	HM6284	14	20	10
U22	74F88	7	14	4,7,13,25
U23	7497	7	14	13,17,18,21
U24	PAL18R4B	10	20	11
U25	74F244	10	20	2,16,24
U26	74ALS845	10	20	15
U27	74F244	10	20	18
U28	127128	14	20	9
U29	74F82	7	14	4,13,16
U30	74F74	7	14	13,16
U31	127128	14	20	9
U32	74F84	7	14	4,8
U33	ALS845	10	20	15
U34	74F244	10	20	14
U35	PAL18R4A	10	20	12
U36	74F244	10	20	14
U37	MC88881	B7	B8	6
U37	MC88881	C3	E9	8
U37	MC88881	E10	E2	8
U37	MC88881	J8	H8	8
U38	MC88820	A10	A9	4
U38	MC88820	B9	D1	4
U38	MC88820	G12	D2	4
U38	MC88820	H13	E3	4
U38	MC88820	J3	G11	4
U38	MC88820	K1	G13	4
U38	MC88820	L7	M8	4
U38	MC88820	N7	N8	4
U39	PAL18R4A	10	20	12
U40	74F244	10	20	14
U41	825123	8	16	12
U42	74LS145	8	16	25

TABLE 1 CONT

REF DES	TYPE	GND	+5V	SH
U43	74F10	7	14	8,25
U44	74F88	7	14	8,16,22,23
U45	PAL18L8A	10	20	18
U46	PAL18L8A	10	20	7
U47	74F74	7	14	20
U48	PAL18L8A	10	20	7
U49	MC88851	C7	B7	5
U49	MC88851	G3	D5	5
U49	MC88851	G11	E10	5
U49	MC88851	L7	G2	5
U49	MC88851	D9	G12	5
U49	MC88851	E4	J4	5
U49	MC88851	J10	K9	5
U49	MC88851	K5	M7	5
U50	74F245	10	20	20
U51	74F244	10	20	19
U52	PAL18L8B	10	20	8
U53	74F245	10	20	20
U54	74F244	10	20	19
U55	PAL18L8A	10	20	17
U56	74F245	10	20	20
U57	74F244	10	20	14
U58	MC88864	30	12	9
U59	74LS244	10	20	23
U60	PAL18L8A	10	20	7
U61	74F84	7	14	4,13,17,18
U62	PAL18L8B	10	20	8
U63	74F244	10	20	19
U64	PAL18L8A	10	20	18
U65	PAL18R4A	10	20	12
U66	74F245	10	20	20
U67	74F244	10	20	19
U68	PAL18R6A	10	20	23
U69	PAL20L8A	12	24	12
U70	20LS2510	10	20	22
U71	74ALS845	10	20	15
U72	74LS244	10	20	23
U73	74LS393	7	14	18
U74	PAL18L8A	10	20	12
U75	74F244	10	20	21
U76	20LS2510	8	16	22
U77	PAL18L8B	10	20	21
U78	PAL18L8A	10	20	8
U79	74F244	10	20	21
U80	MC1455	1	8	17
U81	74F80	7	14	13,21,22,25
U82	PAL18L8A	10	20	21
U83	PAL18L8A	10	20	16
U84	74ALS845	10	20	15
U85	74F32	7	14	8,9,17
U86	PAL18L8A	10	20	23
U87	20LS2510	8	16	22
U88	PAL18R4A	10	20	13
U89	20LS2510	10	20	22
Y1	18MHZ	7	14	10
Y2	16.667MHZ	7	14	4

+
7
6
5
↓
4
3
+



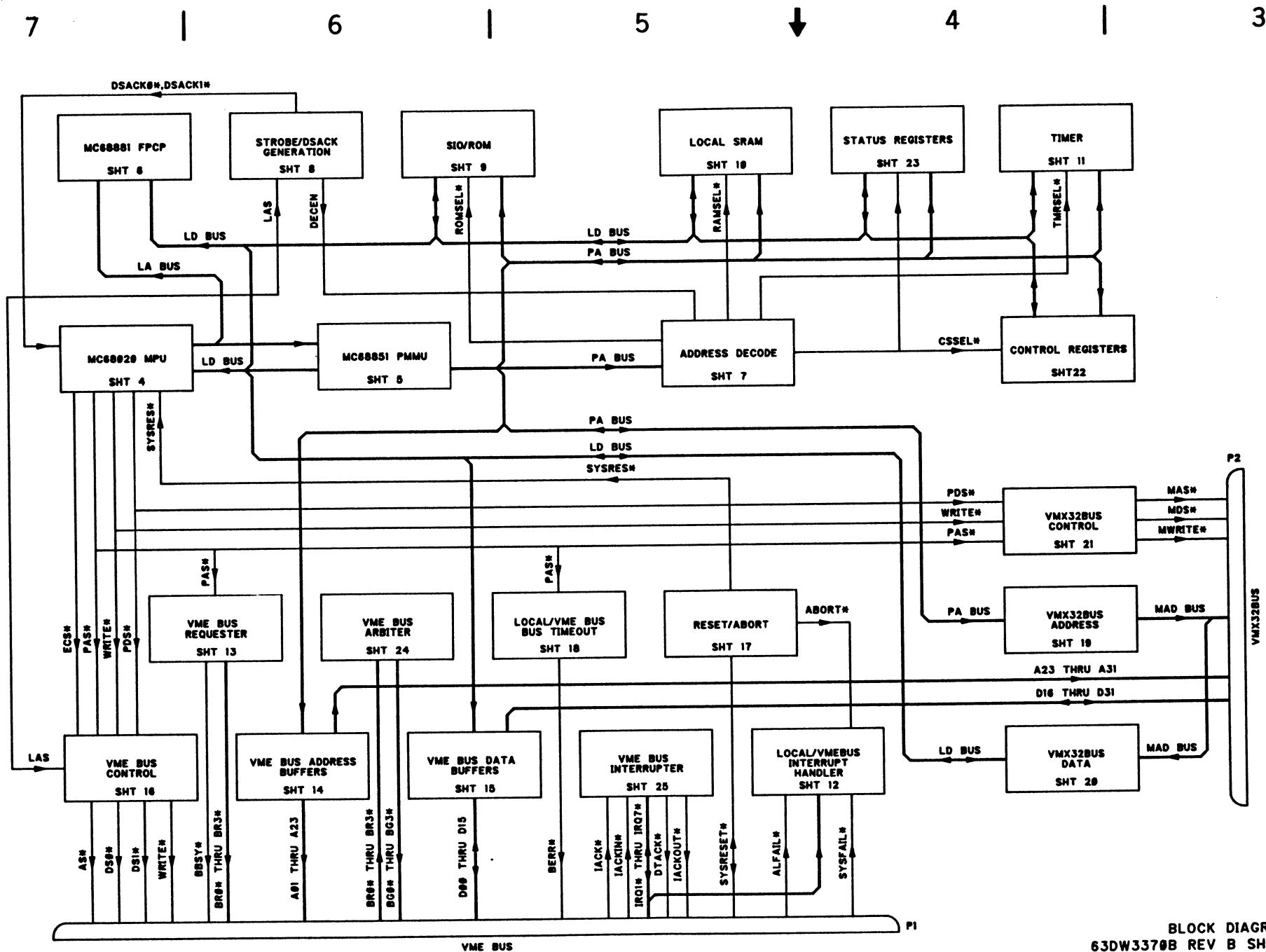
NOT USED

+
7
6
5

63DW3370B REV B SH 2 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram

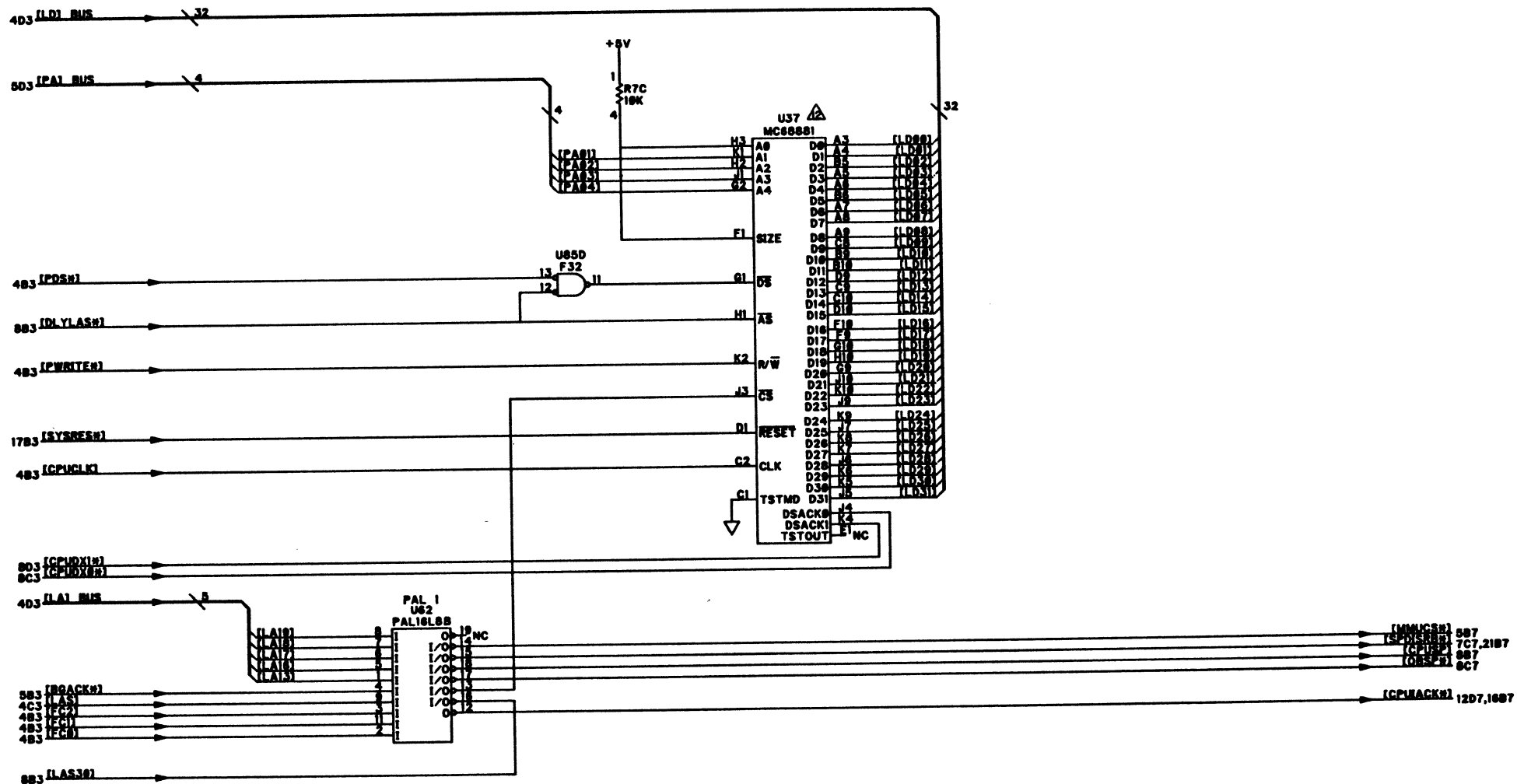
6-17/6-18



BLOCK DIAGRAM
63DW3370B REV B SH 3 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram

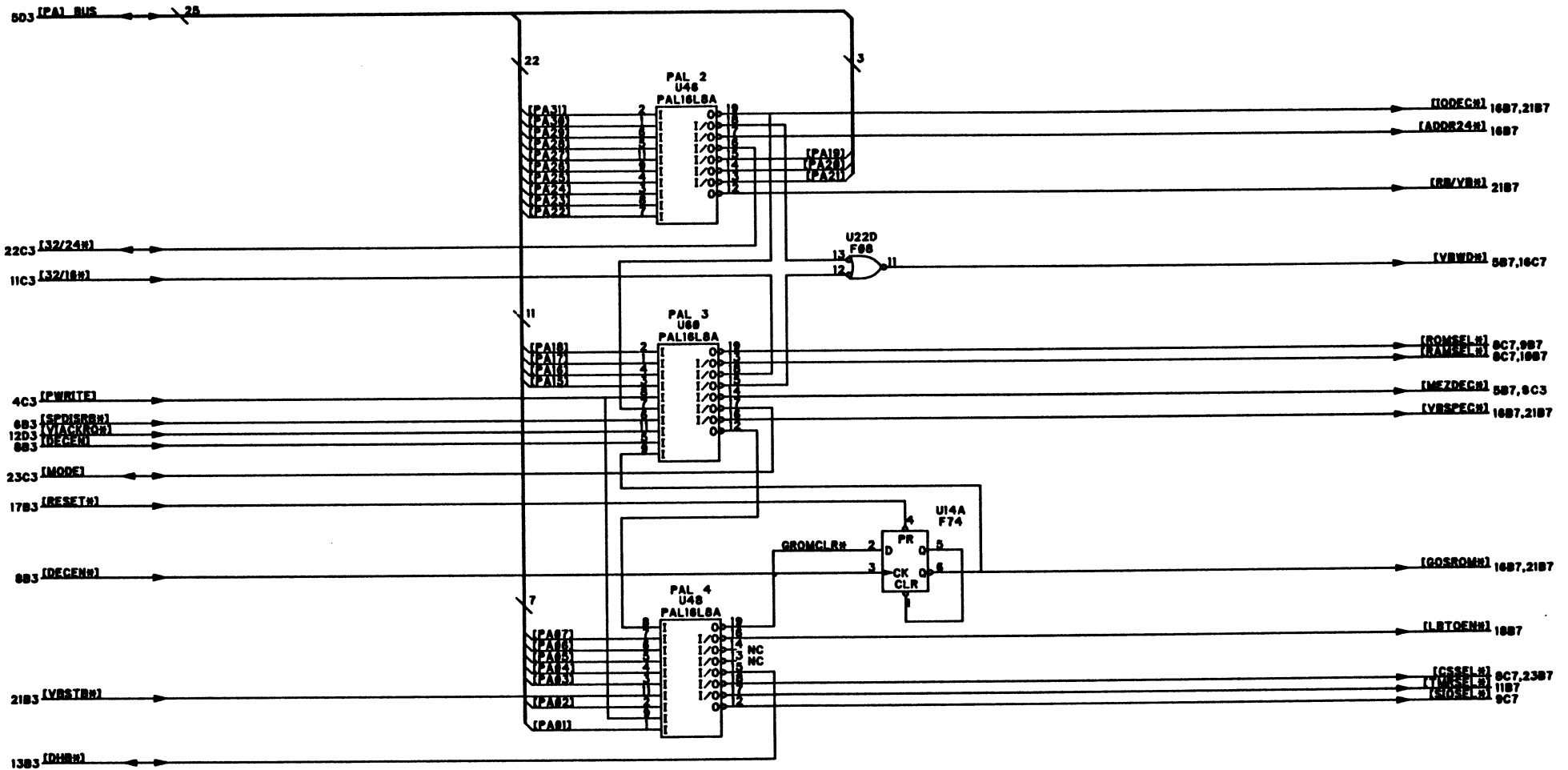
6-19/6-20



68881 COPROCESSOR
63DW3370B REV D SH 6 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram

6-25/6-26



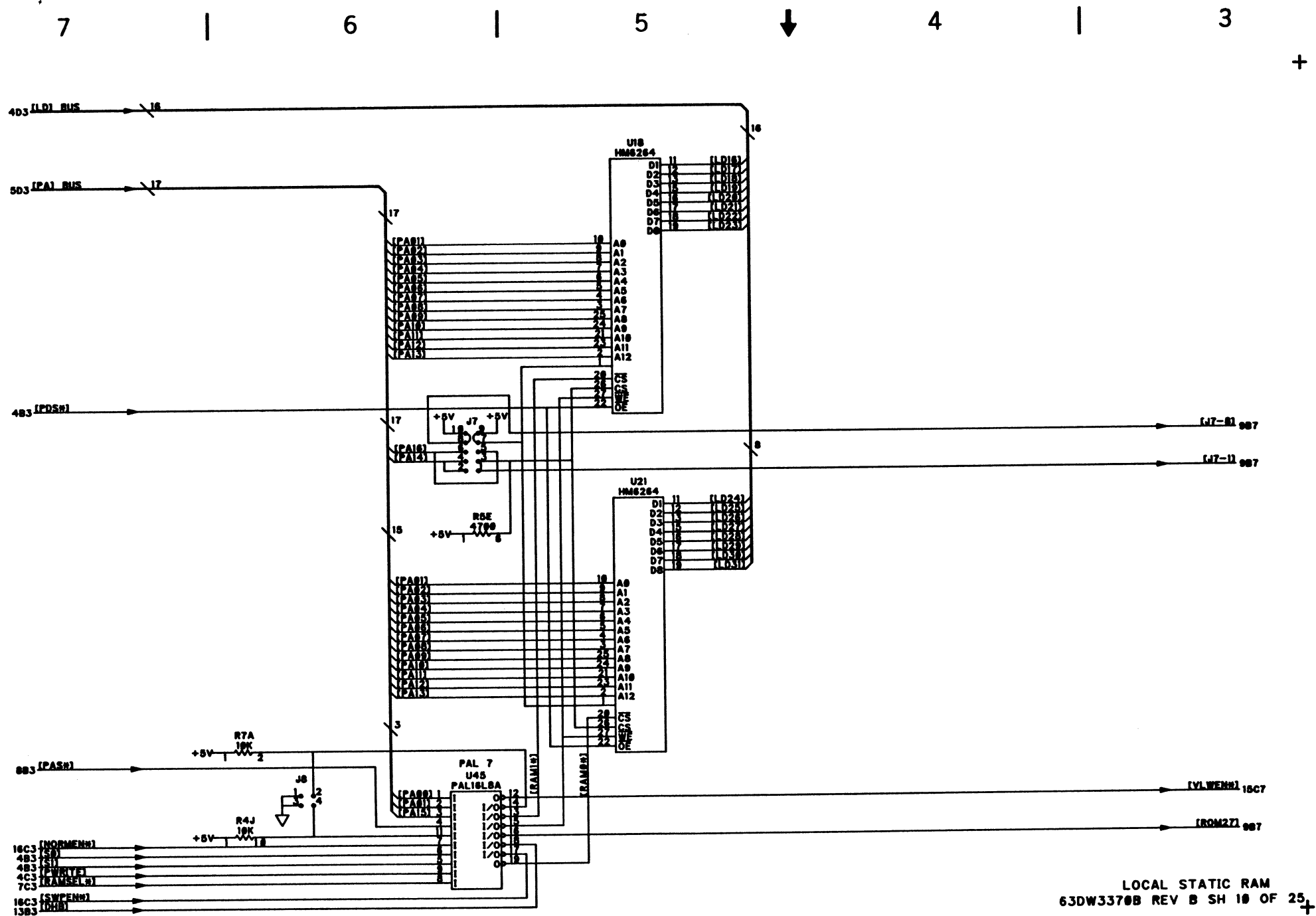
ADDRESS DECODE
63DW3378B REV C SH 7 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram

6-27/6-28



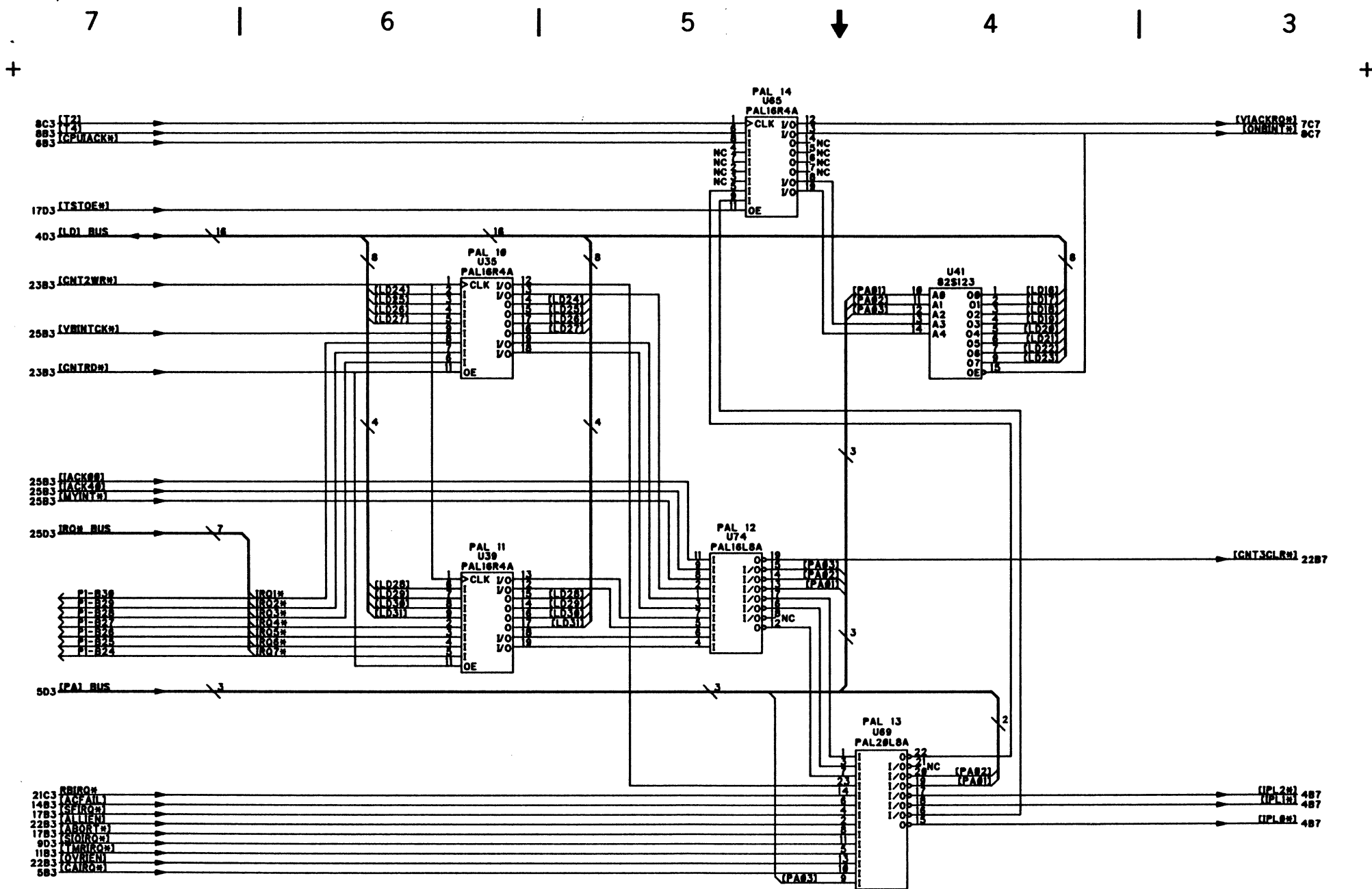
6-31/6-32



LOCAL STATIC RAM
63DW3370B REV B SH 10 OF 25
FIGURE 6-3. VME130 Module Schematic Diagram

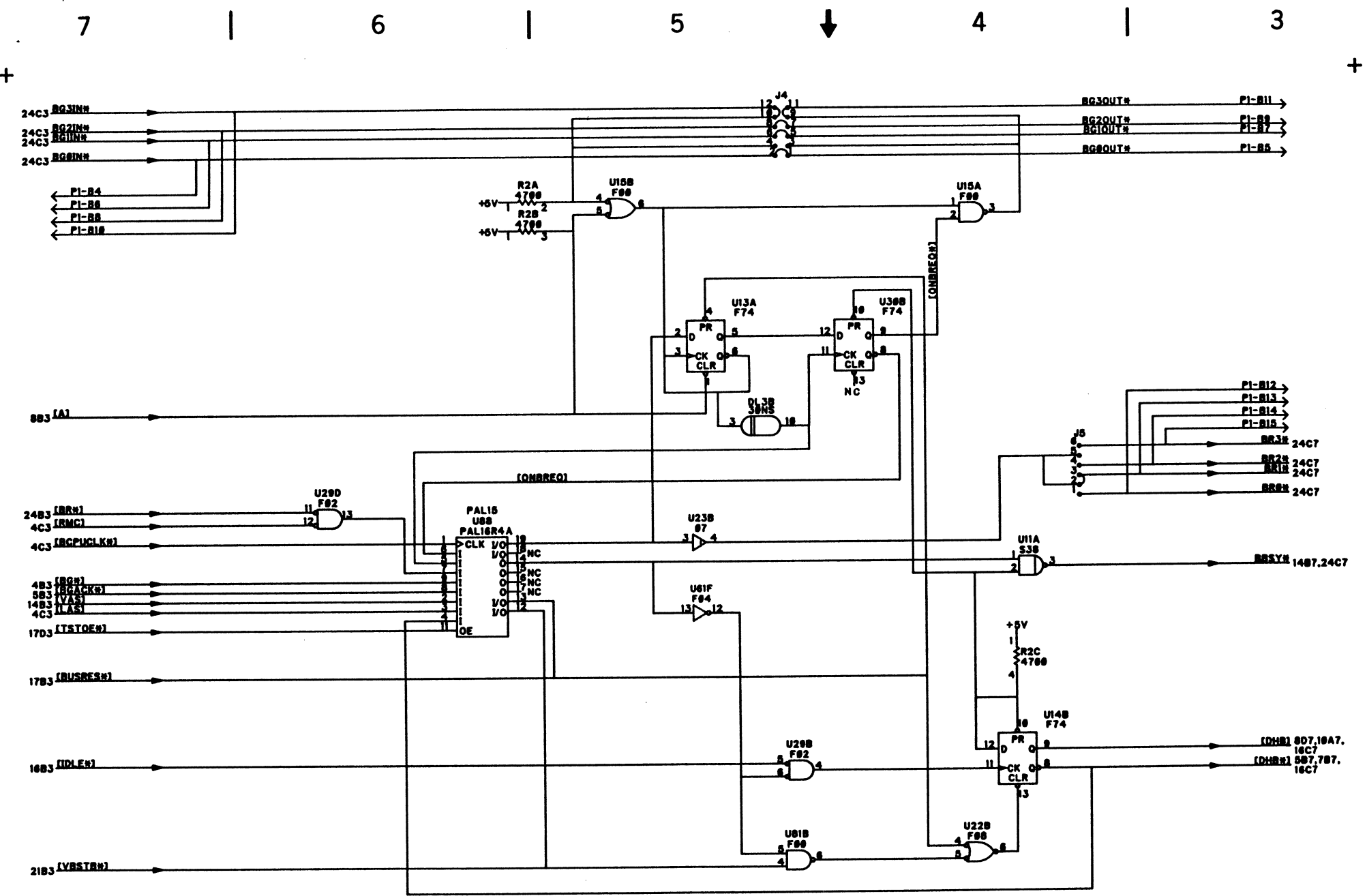


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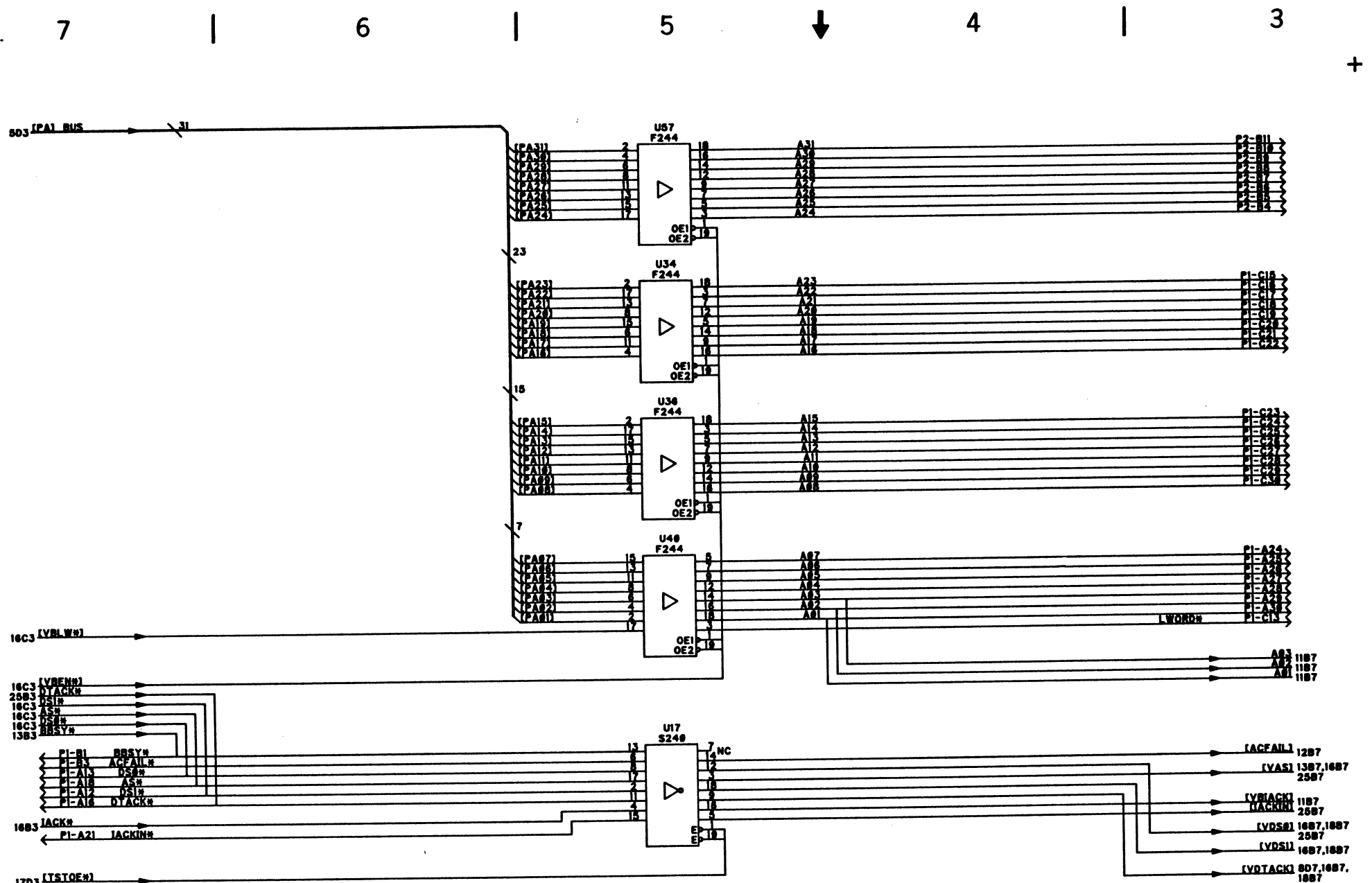
INTERRUPT HANDLER
63DW3370B REV B SH 12 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



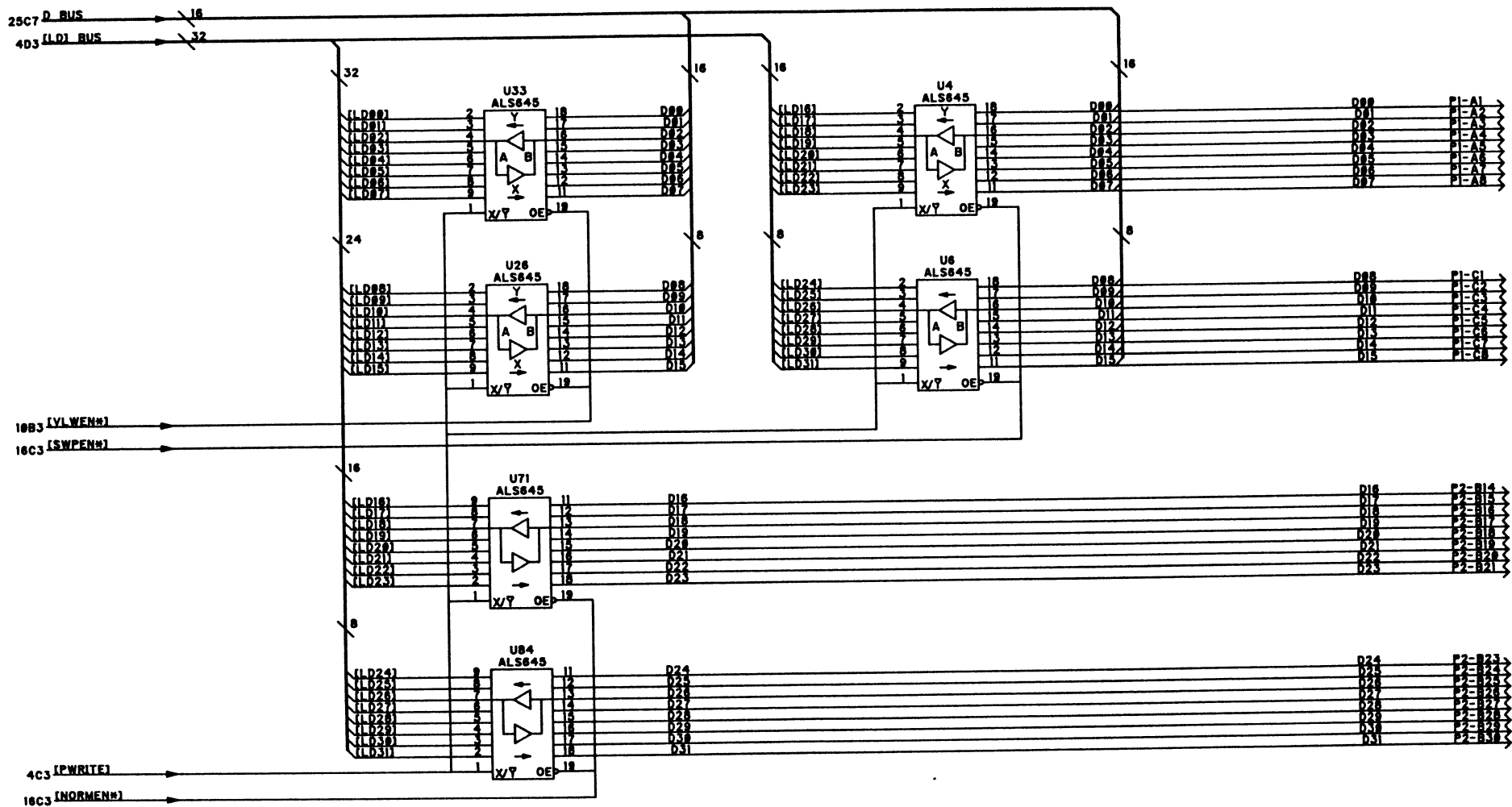
VMEBUS REQUESTER
63DW3370B REV C SH 13 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



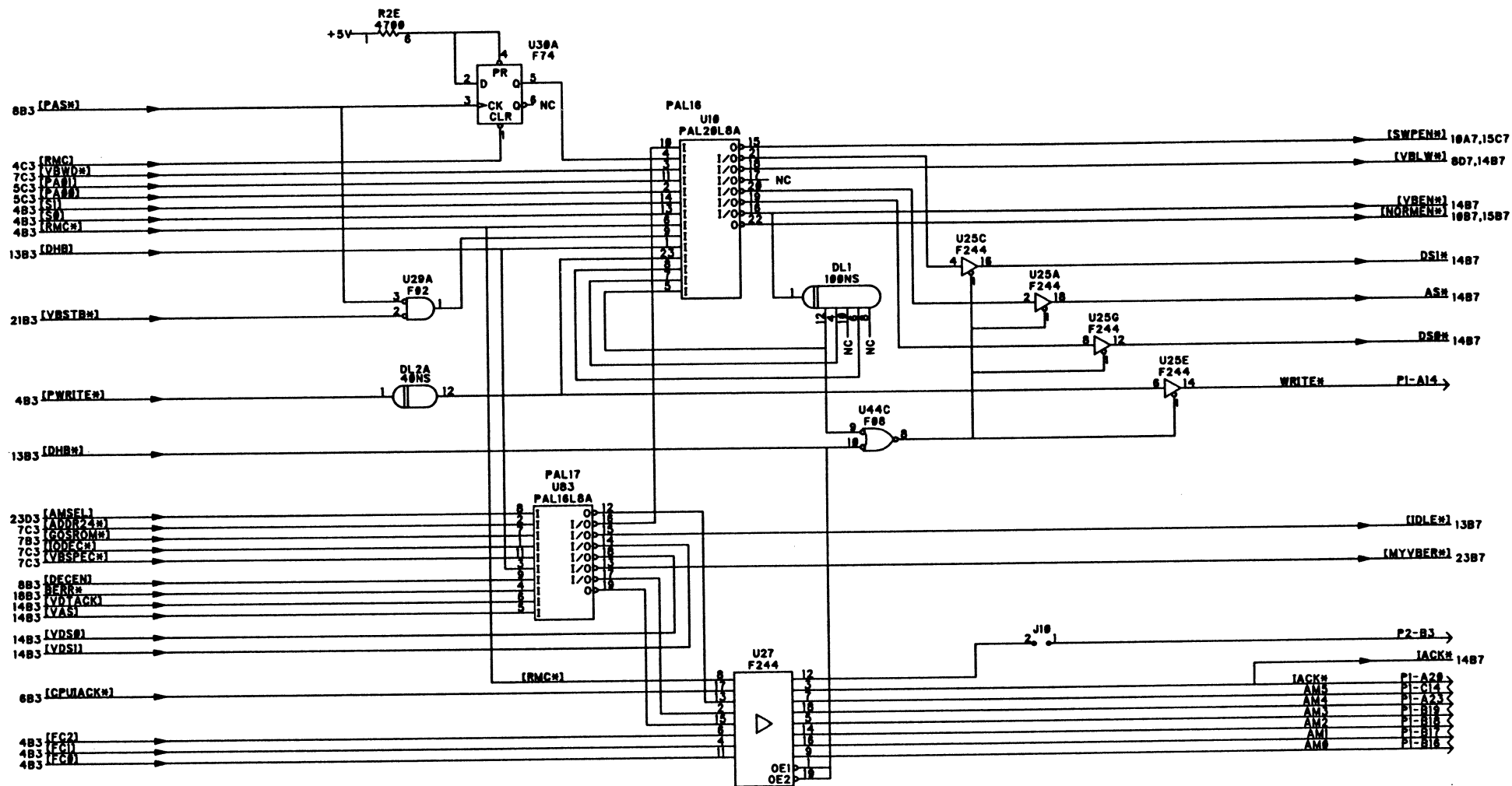
VMEBUS ADDRESS BUFFERS
63DW3370B REV B SH 14 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



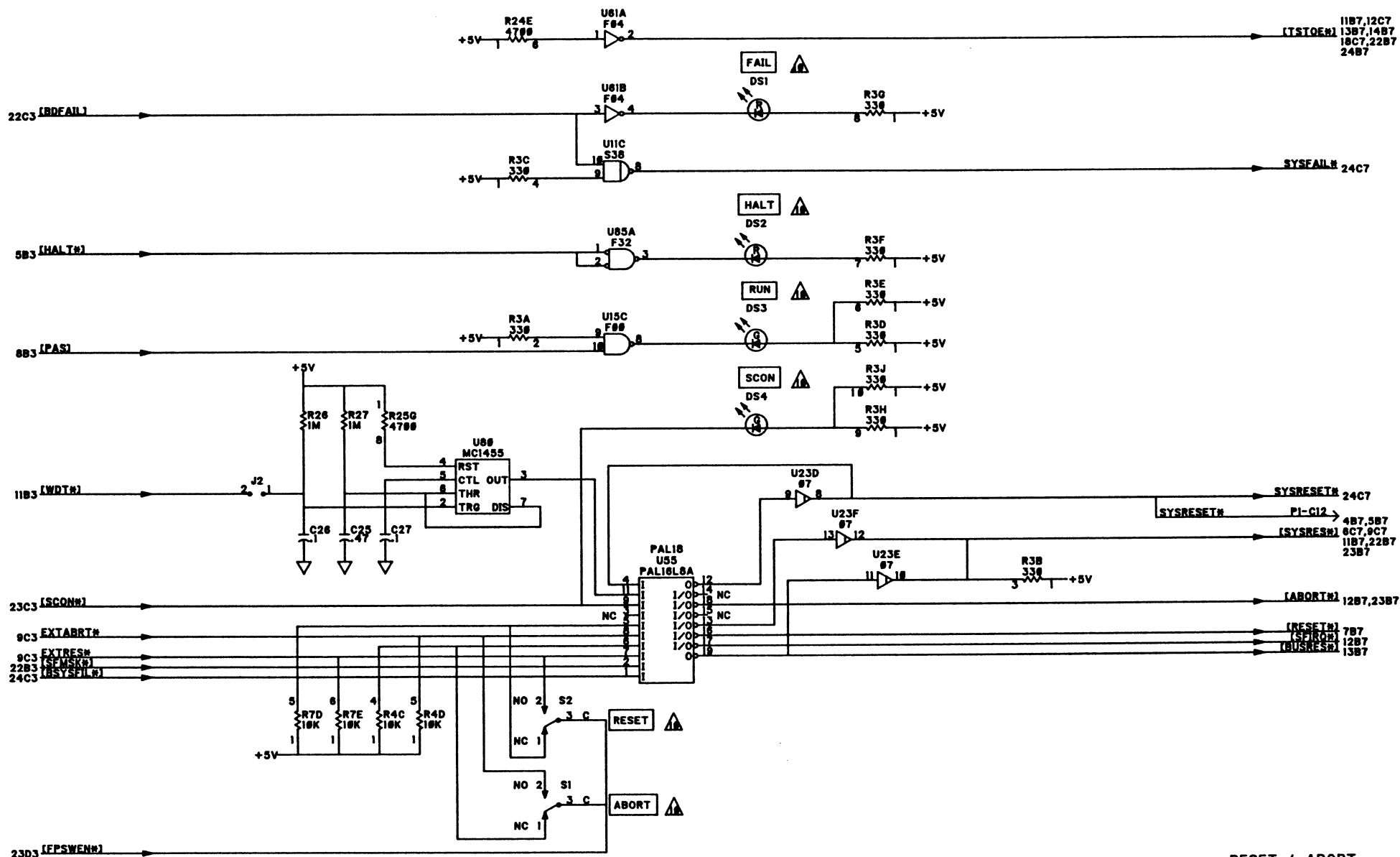
VMEBUS DATA
63DW3370B REV B SH 15 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



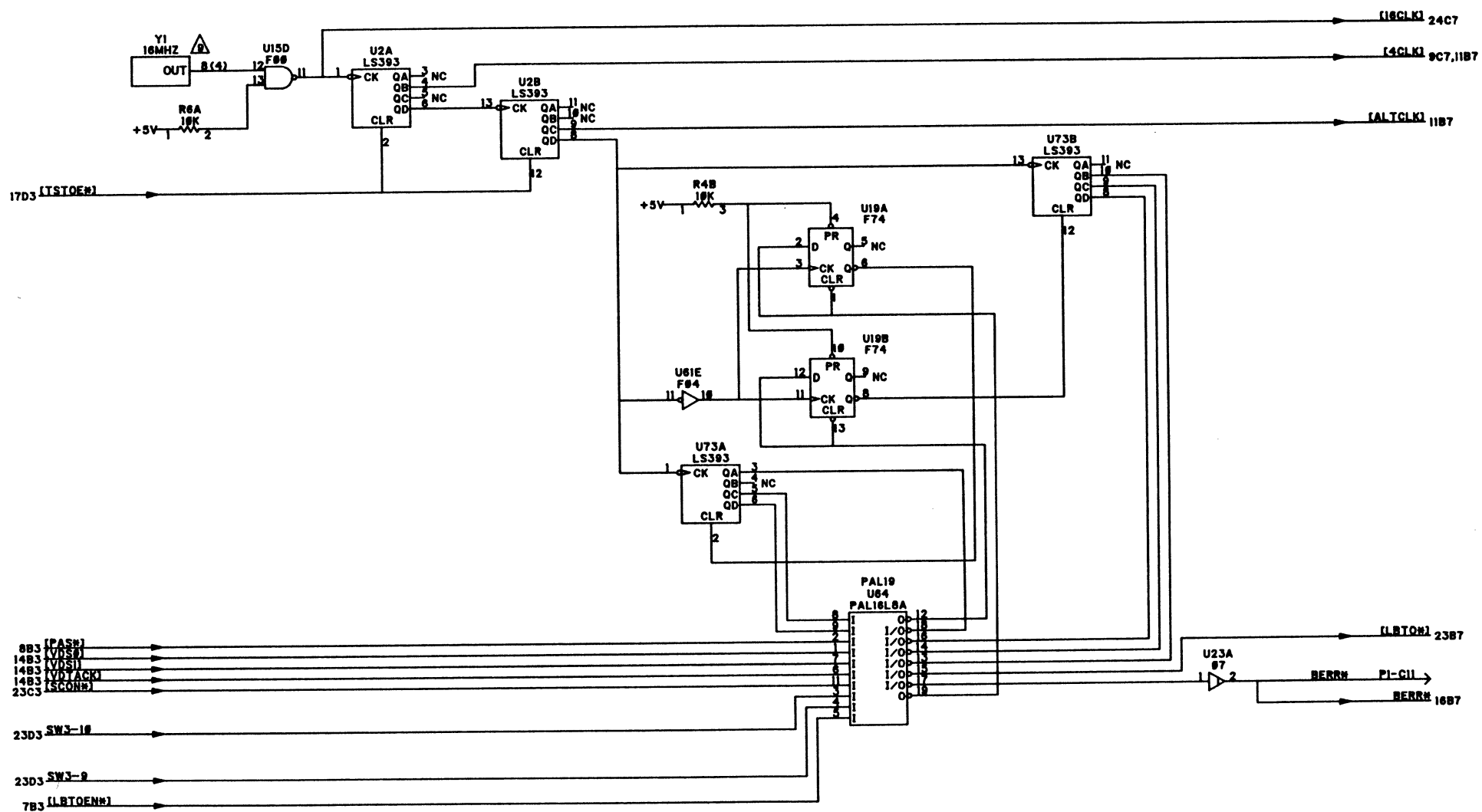
VMEBUS BUS CONTROL
63DW3370B REV C SH 16 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



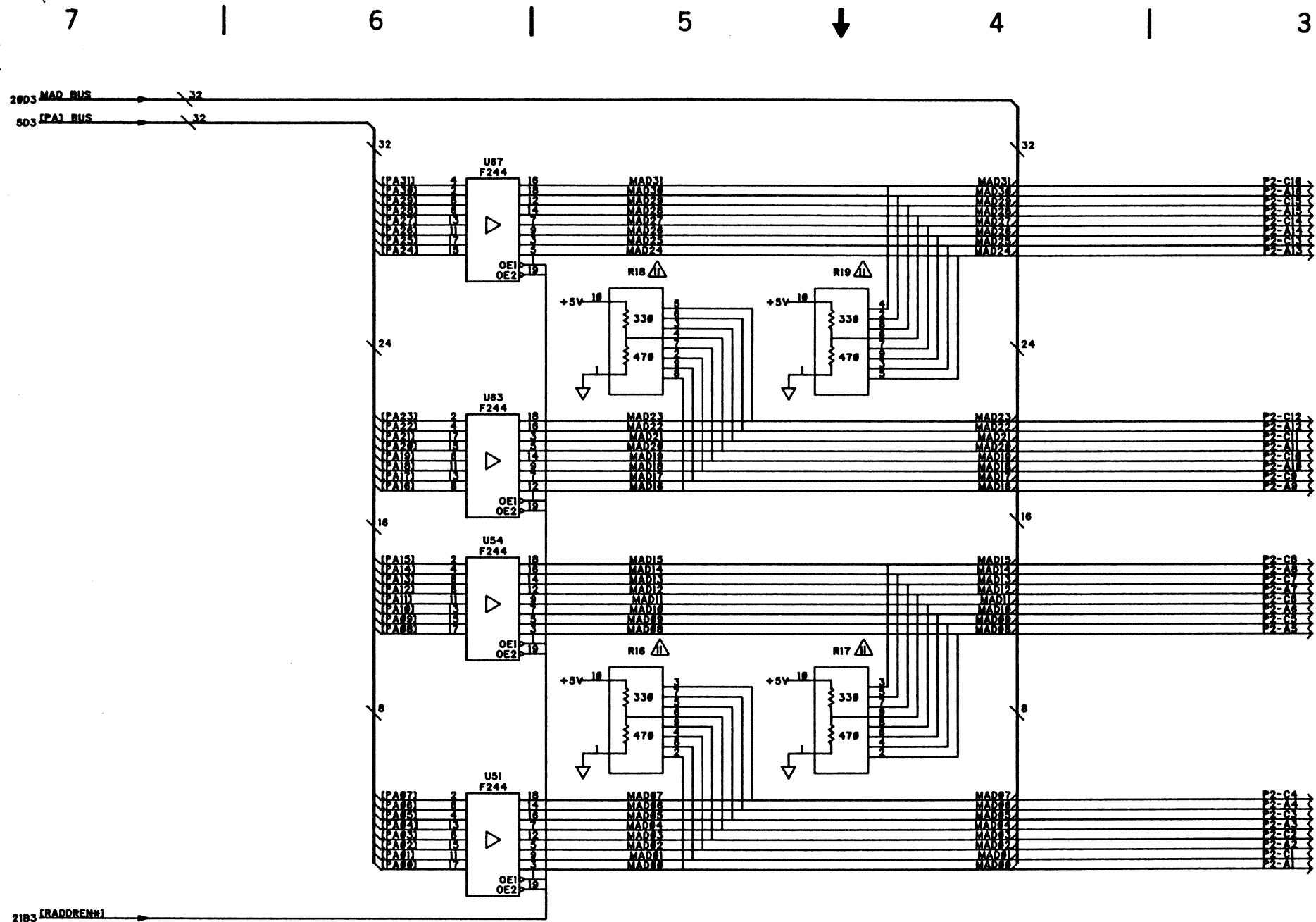
RESET / ABORT
63DW3370B REV B SH 17 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



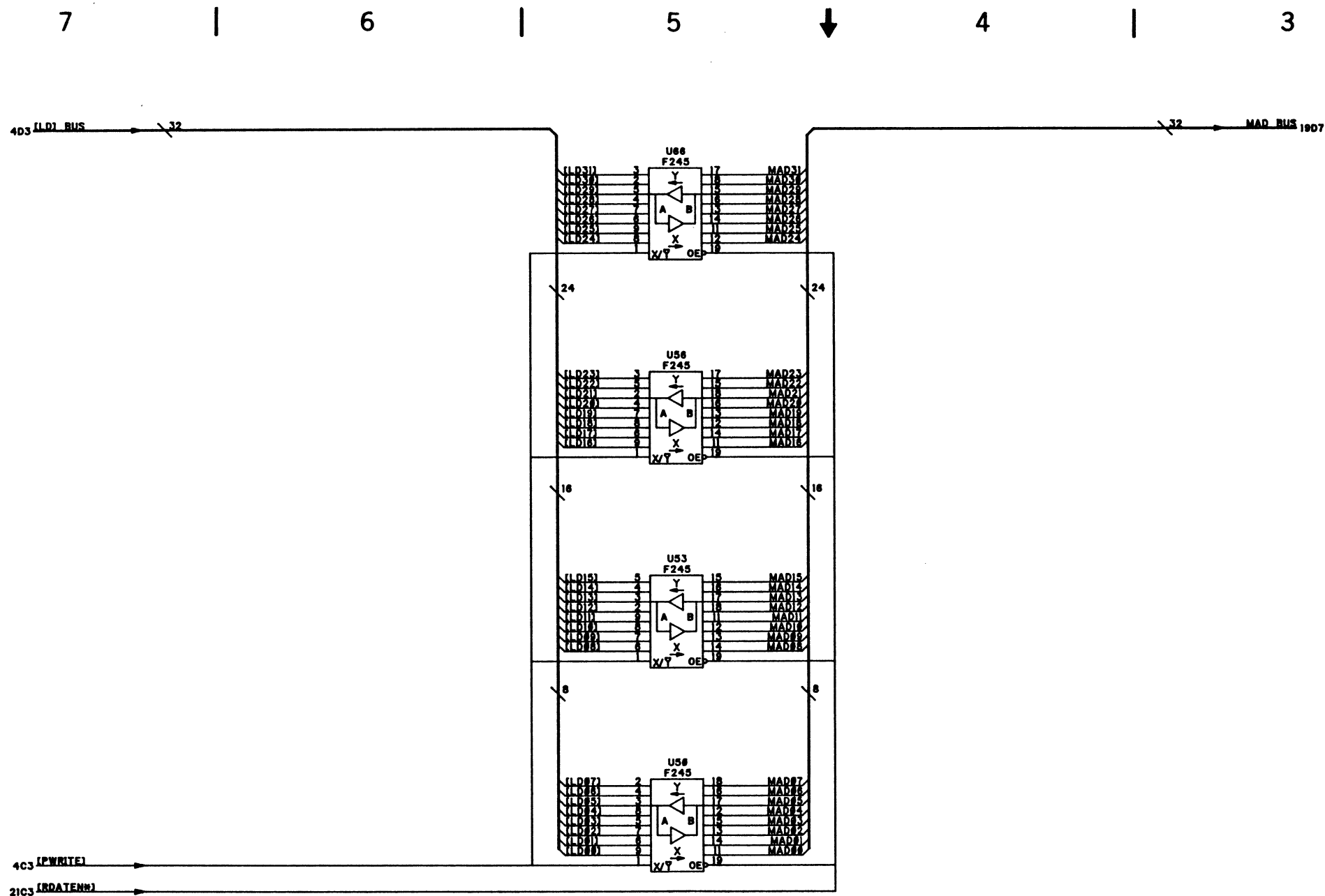
LOCAL RESOURCE AND
VME BUS TIMEOUT
63DW3370B REV D SH 18 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



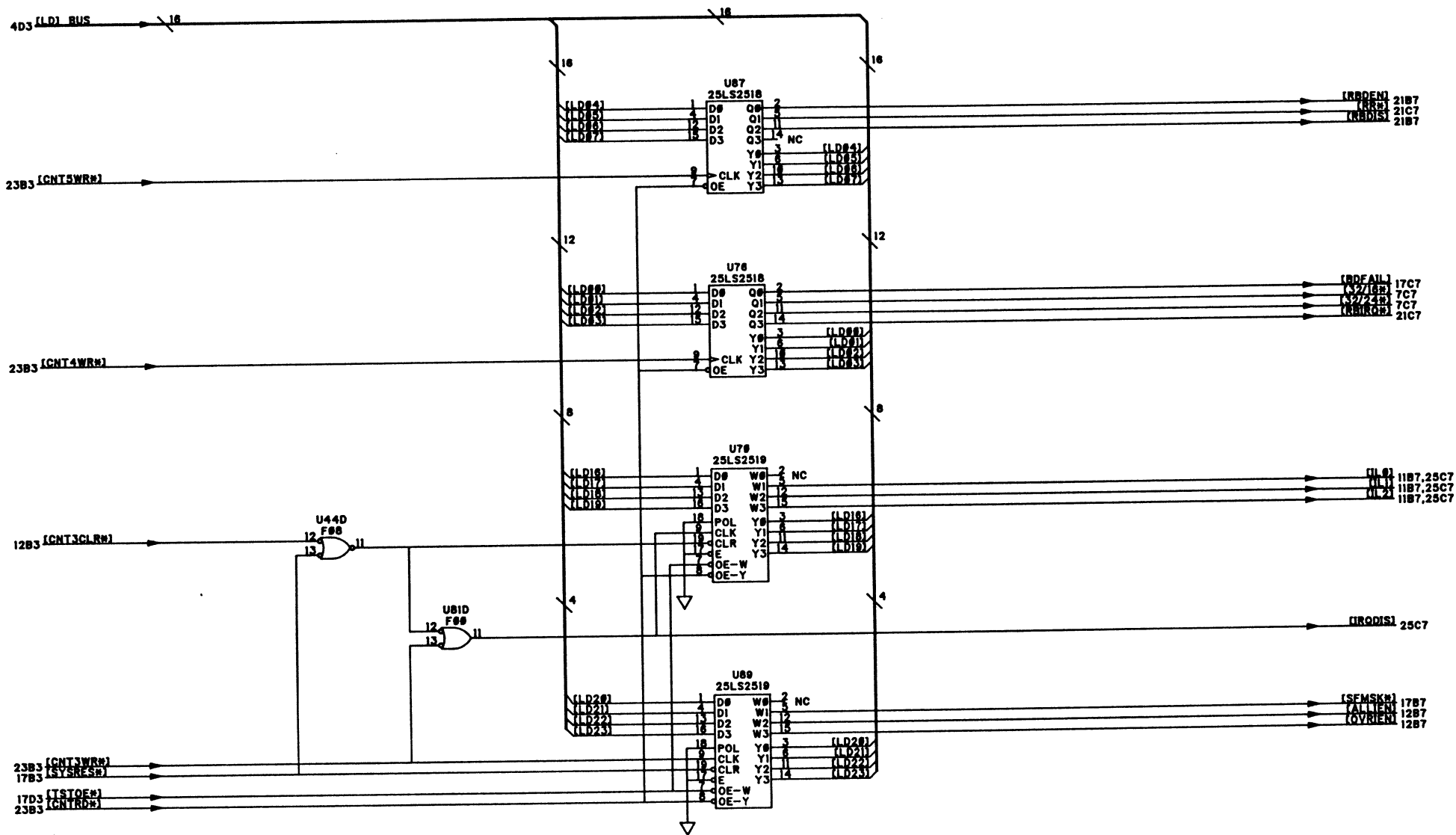
RAMBUS ADDRESS BUFFERS
63DW3370B REV C SH 19 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



RAMBUS DATA BUFFERS
63DW3370B REV B SH 20 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram



CONTROL REGISTERS
63DW3370B REV B SH 22 OF 25

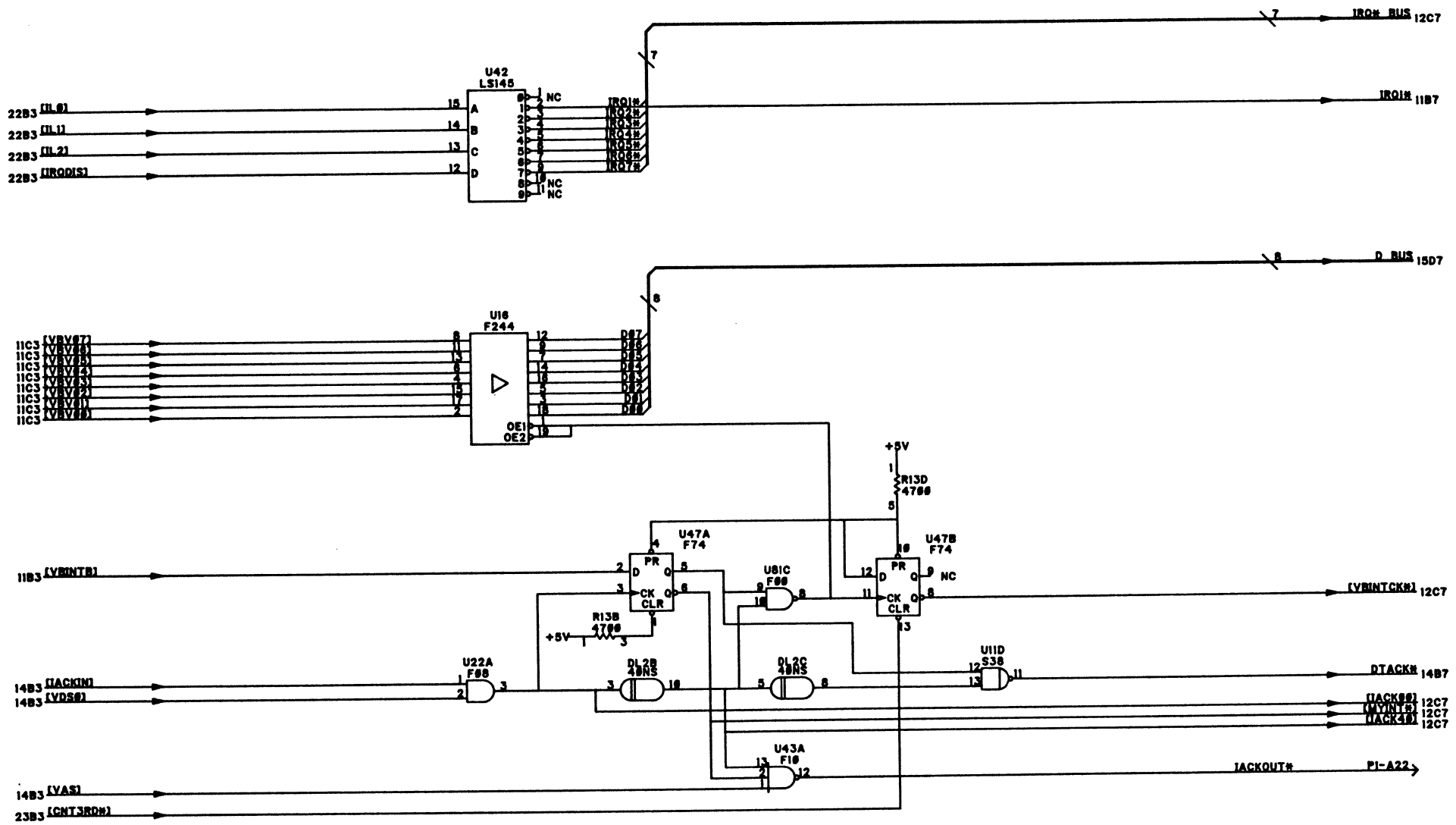
FIGURE 6-3. VME130 Module Schematic Diagram



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6-61/6-62



VMEBUS INTERRUPTER
63DW3370B REV B SH 25 OF 25

FIGURE 6-3. VME130 Module Schematic Diagram